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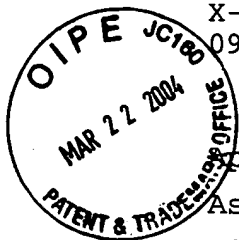
TRANSMITTAL FORM <small>(to be used for all correspondence after initial filing)</small>	Application / Conf. No.	09/684,529 / 1502
	Filing Date	October 06, 2000
	First Named Inventor	John D. Logue
	Examiner Name	Eva Y. Zheng
	Art Unit	2634
	Patent No.	
Mail Stop: NON-FEE AMENDMENT	Attorney Docket Number	X-735 US
Express Mail Receipt No.		
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X-735 US
09/684,529

PATENT
Conf. No. 1502

H6 91
Amended
3-25-04

IN THE UNITED STATES PATENT OFFICE

Applicants: John D. Logue, et al.

Assignee: Xilinx, Inc.

Title: Synchronized Multi-Output Digital Clock Manager

Serial No: 09/684,529 Filed: October 6, 2000

Examiner: Eva Zheng Art Unit: 2634

Docket No: X-735 US Conf. No.:1502

Mail Stop Non-Fee Amendment
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

Applicants submit the following amendment in response to the Office Action dated January 2, 2004.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 7 of this paper.